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Customer No.: 31561
Docket No.: 12119-US-PA
Application No.: 10/710,421

AMENDMENTS

To the Claims:

- 1. (currently amended) A flip chip light-emitting didde package, comprising:
- a Schottky diode comprising;
 - a first conductive type submount having a first surface and a second surface;
- en a first ohmic contact layer, disposed on a portion of the first surface and the second surface of the first conductive type submount; and
- a second ohmic contact layer, disposed on the second surface of the first conductive type submount; and
- a Schottky contact layer, disposed on a <u>another</u> portion of the first surface of the <u>first</u> conductive type submount and electrically contacts directly connected with the <u>first</u> conductive type submount, wherein the ohmic contact layer and the Schottky contact layer are electrically isolated; and
- a light-emitting diode, disposed on the Schottky diode by flip-chip bonding process, wherein the light-emitting diode and the Schottky diode are connected reverse and in parallel.
- 2. (original) The flip chip light-emitting diode package of claim 1, wherein the package further comprises a plurality of solder bumps disposed between the Schottky diode and the light-emitting diode so that the Schottky diode and the light-emitting diode are connected reverse and in parallel.

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3. (original) The flip chip light-emitting diode package of claim 1, wherein the light-emitting diode further comprises:

a substrate;

a semiconductor layer, disposed on the substrate, wherein the semiconductor layer at least comprises a first conductive type doped semiconductor layer, a second conductive type doped semiconductor layer and a light-emitting layer, and the light-emitting layer is disposed on the first conductive type doped semiconductor layer and the second conductive type doped semiconductor layer is disposed on the light-emitting layer:

- a first electrode, disposed on the first conductive type doped semiconductor layer; and a second electrode, disposed on the second conductive type doped semiconductor layer.
- 4. (original) The flip chip light-emitting diode rackage of claim 3, wherein material forming the first electrode is selected from a group consisting of Ti/Al, Cr/Au, Cr/Pt/Au, Cr/Pd/Au and Cr/Ti/Au.
- 5. (original) The flip chip light-emitting diode package of claim 3, wherein material forming the second electrode is selected from a group consisting of Ni/Au, Pd/Au, Pt/Au, Ti/Au, Cr/Au, Sn/Au and Ta/Au.
- 6. (original) The flip chip light-emitting diode package of claim 3, wherein the second electrode comprises an N-type transparent conductive oxide layer or a P-type transparent conductive oxide layer.

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7. (currently amended) The flip chip light-emitting diode package of claim 3 6, wherein material constituting the N-type transparent conductive oxide layer comprises ITO or CTO.

8. (currently amended) The flip chip light-emitting diode package of claim 3 6, wherein material constituting the P-type transparent conductive oxide layer comprises CuAlO₂ or SrCu₂O₂.

Claim 9. (canceled)

10. (currently amended) The flip chip light-emitting diode package of claim 1, wherein the first conductive type submount comprises an N-doped material.

11. (currently amended) The flip chip light-emitting diode package of claim 1, wherein the first conductive type submount comprises a P-doped material.

12. (currently amended) The flip chip light-emitting diode package of claim 1, wherein material forming the <u>first conductive type</u> submount is selected from a group consisting of Si, GaAs, GaP, GaN and ZnO.

13. (previously presented) The flip chip light-emitting diode package of claim 1, wherein material forming the ohmic contact layer comprises aluminum (Al).

14. (previously presented) The flip chip light-emitting diode package of claim 1, wherein material forming the Schottky contact layer is selected from a group consisting of titanium (Ti), nickel (Ni), gold (Au), tungsten (W), silver (Ag) and platinum (Pt).

15. (currently amended) A flip chip light-emitting diode package, comprising:

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a Schottky diode group having a plurality of Schottky diodes, wherein the Schottky diodes are electrically connected in series, in parallel or ir series and parallel together, each of the Schottky diodes comprises:

a first conductive type submount having a first surface and a second surface;

an a first ohmic contact layer, disposed on a portion of the first surface and the second surface of the first conductive type submount; and

a second ohmic contact layer, disposed on the second surface of the first conductive type submount; and

a Schottky contact layer, disposed on a another portion of the first surface of the first conductive conductive type submount and electrically contacts directly connected with the first conductive type submount, wherein the ohmic contact layer and the Schottky contact layer are electrically isolated; and

a light-emitting diode disposed on one of the Schottky diodes by flip-chip bonding process, wherein the light-emitting diode and the Schottky diode group are connected reverse and in parallel.

16. (original) The flip chip light-emitting diode package of claim 15, wherein the package further comprises a plurality of solder bumps disposed between one of the Schottky diodes and the light-emitting diode so that the Schottky diode and the light-emitting diode are connected reverse and in parallel.

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17. (original) The flip chip light-emitting diode package of claim 15, wherein the light-emitting diode further comprises:

a substrate;

a semiconductor layer, disposed on the substrate, wherein the semiconductor layer at least comprises a first conductive type doped semiconductor layer, a second conductive type doped semiconductor layer and a light-emitting layer, and the light-emitting layer is disposed on the first conductive type doped semiconductor layer and the second conductive type doped semiconductor layer is disposed on the light-emitting layer;

- a first electrode, disposed on the first conductive type doped semiconductor layer; and a second electrode, disposed on the second conductive type doped semiconductor layer.
- 18. (original) The flip chip light-emitting diode package of claim 17, wherein material forming the first electrode is selected from a group consisting of Ti/Al, Cr/Au, Cr/Pt/Au, Cr/Pd/Au and Cr/Ti/Au.
- 19. (original) The flip chip light-emitting diode package of claim 17, wherein material forming the second electrode is selected from a group consisting of Ni/Au, Pd/Au, Pt/Au, Ti/Au, Cr/Au, Sn/Au and Ta/Au.
- 20. (original) The flip chip light-emitting diode package of claim 17, wherein the second electrode comprises an N-type transparent conductive oxide layer or a P-type transparent conductive oxide layer.

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- 21. (currently amended) The flip chip light-emitting diode package of claim 17 20, wherein material constituting the N-type transparent conductive oxide layer comprises ITO or CTO.
- 22. (currently amended) The flip chip light-emitting diode package of claim $\frac{17}{20}$, wherein material constituting the P-type transparent conductive oxide layer comprises CuAlO₂ or $SrCu_2O_2$.

Claim 23. (canceled)

- 24. (currently amended) The flip chip light-emitting diode package of claim 15, wherein the first conductive type submount comprises an N-doped material.
- 25. (currently amended) The flip chip light-emitting diode package of claim 15, wherein the first conductive type submount comprises a P-doped material.
- 26. (currently amended) The flip chip light-emitting diode package of claim 15, wherein material forming the <u>first conductive type</u> submount is selected from a group consisting of Si, GaAs, GaP, GaN and ZnO.
- 27. (previously presented) The flip chip light-emitting diode package of claim 15, wherein material forming the ohmic contact layer comprises aluminum (Al).
- 28. (previously presented) The flip chip light-emitting diode package of claim 15, wherein material forming the Schottky contact layer is selected from a group consisting of titanium (Ti), nickel (Ni), gold (Au), tungsten (W), silver (Ag) and platinum (Pt).

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